

Cmos Digital Integrated Circuits Kang Solution

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

In two editions spanning more than a decade, *The Electrical Engineering Handbook* stands as the definitive reference to the multidisciplinary field of electrical engineering. Our knowledge continues to grow, and so does the Handbook. For the third edition, it has expanded into a set of six books carefully focused on a specialized area or field of study. *Electronics, Power Electronics, Optoelectronics, Microwaves, Electromagnetics, and Radar* represents a concise yet definitive collection of key concepts, models, and equations in these areas, thoughtfully gathered for convenient access.

Electronics, Power Electronics, Optoelectronics, Microwaves, Electromagnetics, and Radar delves into the fields of electronics, integrated circuits, power electronics, optoelectronics, electromagnetics, light waves, and radar, supplying all of the basic information required for a deep understanding of each area. It also devotes a section to electrical effects and devices and explores the emerging fields of microlithography and power electronics. Articles include defining terms, references, and sources of further information. Encompassing the work of the world's foremost experts in their respective specialties, Electronics, Power Electronics, Optoelectronics, Microwaves, Electromagnetics, and Radar features the latest developments, the broadest scope of coverage, and new material in emerging areas.

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?: Analog MOS integrated circuits for signal processing/Roubik Gregorian, Gabor C. Temes. -- Wiley, 1986

An accessible guide to how semiconductor electronics work and how they are manufactured, for professionals and interested readers with no electronics engineering background Semiconductor Basics is an accessible guide to how semiconductors work. It is written for readers without an electronic engineering background. Semiconductors are the basis for almost all modern electronic devices. The author—an expert on the topic—explores the fundamental concepts of what a semiconductor is, the different types in use, and how they are different from conductors and insulators. The book has a large number of helpful and illustrative drawings, photos, and figures. The author uses only simple arithmetic to help understand the device operation and applications. The book reviews the key devices that can be constructed using semiconductor materials such as diodes and transistors and all the large electronic systems based on these two component such as computers, memories, LCDs and related technology like Lasers LEDs and infrared detectors. The text also explores integrated circuits and explains how they are fabricated. The author concludes with some projections about what can be expected in the future. This important book: Offers an accessible guide to semiconductors using qualitative explanations and analogies, with minimal mathematics and equations Presents the material in a well-structured and logical format Explores topics from device physics fundamentals to transistor formation and fabrication and the operation of the circuits to build electronic devices and systems Includes information on practical applications of p-n junctions, transistors, and integrated circuits to link theory and practice Written for anyone interested in the technology, working in semiconductor labs or in the semiconductor industry, Semiconductor Basics offers clear explanations about how semiconductors work and its manufacturing process.

This book constitutes the refereed proceedings of the 14th International Workshop on Power and Timing Optimization and Simulation, PATMOS 2004, held in Santorini, Greece in September 2004. The 85 revised papers presented together with abstracts of 6 invited presentations were carefully reviewed and selected from 152 papers submitted. The papers are organized in topical sections on buses and communication, circuits and devices, low power issues, architectures, asynchronous circuits, systems design, interconnect and physical design, security and safety, low-power processing, digital design, and modeling and simulation.

There is arguably no field in greater need of a comprehensive handbook than computer engineering. The unparalleled rate of technological advancement, the explosion of computer applications, and the now-in-progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own. References published only a few years ago are now sorely out of date. The Computer Engineering Handbook changes all of that. Under the leadership of Vojin Oklobdzija and a stellar editorial board, some of the industry's foremost experts have joined forces to create what promises to be the definitive resource for computer design and engineering. Instead of focusing on basic, introductory material, it forms a comprehensive, state-of-the-art review of the field's most recent achievements, outstanding issues, and future directions. The world of computer engineering is vast and evolving so rapidly that what is cutting-edge today may be obsolete in a few months. While exploring the new developments, trends, and future directions of the field, The Computer Engineering Handbook captures what is fundamental and of lasting value.

This book presents the design of ultra-wideband (UWB) impulse-based transmitter and receiver frontends, operating within the 3.1-10.6 GHz frequency band, using CMOS radio-frequency integrated-circuits (RFICs). CMOS RFICs are small, cheap, low power devices, better suited

for direct integration with digital ICs as compared to those using III-V compound semiconductor devices. CMOS RFICs are thus very attractive for RF systems and, in fact, the principal choice for commercial wireless markets. The book comprises seven chapters. The first chapter gives an introduction to UWB technology and outlines its suitability for high resolution sensing and high-rate, short-range ad-hoc networking and communications. The second chapter provides the basics of CMOS RFICs needed for the design of the UWB RFIC transmitter and receiver presented in this book. It includes the design fundamentals, lumped and distributed elements for RFIC, layout, post-layout simulation, and measurement. The third chapter discusses the basics of UWB systems including UWB advantages and applications, signals, basic modulations, transmitter and receiver frontends, and antennas. The fourth chapter addresses the design of UWB transmitters including an overview of basic components, design of pulse generator, BPSK modulator design, and design of a UWB tunable transmitter. Chapter 5 presents the design of UWB receivers including the design of UWB low-noise amplifiers, correlators, and a UWB 1 receiver. Chapter 6 covers the design of a UWB uniplanar antenna. Finally, a summary and conclusion is given in Chapter 7.

The volume includes a set of selected papers extended and revised from the 2011 International Conference on Mechanical Engineering and Technology, held on London, UK, November 24-25, 2011. Mechanical engineering technology is the application of physical principles and current technological developments to the creation of useful machinery and operation design. Technologies such as solid models may be used as the basis for finite element analysis (FEA) and / or computational fluid dynamics (CFD) of the design. Through the application of computer-aided manufacturing (CAM), the models may also be used directly by software to create "instructions" for the manufacture of objects represented by the models, through computer numerically controlled (CNC) machining or other automated processes, without the need for intermediate drawings. This volume covers the subject areas of mechanical engineering and technology, and also covers interdisciplinary subject areas of computers, communications, control and automation. We hope that researchers, graduate students and other interested readers benefit scientifically from the book and also find it stimulating in the process.

With the advance of semiconductors and ubiquitous computing, the use of system-on-a-chip (SoC) has become an essential technique to reduce product cost. With this progress and continuous reduction of feature sizes, and the development of very large-scale integration (VLSI) circuits, addressing the harder problems requires fundamental understanding of circuit and layout design issues. Furthermore, engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer-aided design (CAD) tools. Introduction to VLSI Systems: A Logic, Circuit, and System Perspective addresses the need for teaching such a topic in terms of a logic, circuit, and system design perspective. To achieve the above-mentioned goals, this classroom-tested book focuses on: Implementing a digital system as a full-custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal-oxide-semiconductor (CMOS) VLSI Important issues of modern CMOS processes, including deep submicron devices, circuit optimization, interconnect modeling and optimization, signal integrity, power integrity, clocking and timing, power dissipation, and electrostatic discharge (ESD) Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up, paying much attention to logic circuit, layout, and system designs. Armed with these tools, readers can not only comprehensively understand the features and limitations of modern VLSI technologies, but also have enough background to adapt to this ever-changing field.

Offers comprehensive coverage of digital CMOS circuit design, as well as addressing technology issues highlighted by the widespread use of nanometer-scale CMOS technologies.

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The second edition of this comprehensive text contains extensive revisions to reflect recent advances in technology and in circuit design practices. Recognizing that the area of digital integrated circuit design is evolving at an increasingly fast pace, every effort has been made to present state-of-the-art material on all subjects covered in the book. This book is primarily designed as a comprehensive text for senior level and first-year graduate level digital circuit design classes, as well as a reference for practicing engineers in the areas of IC design and VLSI. Summarizes the schemes and technologies in RF circuit design, describes the basic parameters of an RF system and the fundamentals of RF system design, and presents an introduction of the individual RF circuit block design. Forming the backbone of today's mobile and satellite communications networks, radio frequency (RF) components and circuits are incorporated into everything that transmits or receives a radio wave, such as mobile phones, radio, WiFi, and walkie talkies. RF Circuit Design, Second Edition immerses practicing and aspiring industry professionals in the complex world of RF design. Completely restructured and reorganized with new content, end-of-chapter exercises, illustrations, and an appendix, the book presents integral information in three complete sections: Part One explains the different methodologies between RF and digital circuit design and covers voltage and power transportation, impedance matching in narrow-band case and wide-band case, gain of a raw device, measurement, and grounding. It also goes over equipotentiality and current coupling on ground surface, as well as layout and packaging, manufacturability of product design, and radio frequency integrated circuit (RFIC). Part Two includes content on the main parameters and system analysis in RF circuit design, the fundamentals of differential pair and common-mode rejection ratio (CMRR), Balun, and system-on-a-chip (SOC). Part Three covers low-noise amplifier (LNA), power amplifier (PA), voltage-controlled oscillator (VCO), mixers, and tunable filters. RF Circuit Design, Second Edition is an ideal book for engineers and managers who work in RF circuit design and for courses in electrical or electronic engineering.

In 1993, the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work. Now, this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today. Every electrical engineer should have an opportunity to expand his expertise with this definitive guide. In a single volume, this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry, government, or academia. This well-organized book is divided into 12 major sections that encompass the entire field of electrical engineering, including circuits, signal processing, electronics, electromagnetics, electrical effects and devices, and energy, and the emerging trends in the fields of communications, digital devices, computer engineering, systems, and biomedical engineering. A compendium of physical, chemical, material, and mathematical data completes this comprehensive resource. Every major topic is thoroughly covered and every important concept is defined, described, and illustrated. Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer, researchers, and students. A distinguished advisory board and contributors including many of the leading authors, professors, and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field. No other single volume available today offers this combination of broad coverage and depth of exploration of the topics. The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for years to come.

Monolithic Microwave Integrated Circuit (MMIC) is an electronic device that is widely used in all high frequency wireless systems. In developing MMIC as a product, understanding analysis and design techniques, modeling, measurement methodology, and current trends are essential. Advances in Monolithic Microwave Integrated Circuits for Wireless Systems: Modeling and Design Technologies is a central source of knowledge on MMIC development, containing research on theory, design, and practical approaches to integrated circuit devices. This book is of interest to researchers in industry and academia working in the areas of circuit design, integrated circuits, and RF and microwave, as well as anyone with an interest in monolithic wireless device

development.

This book details timing analysis and optimization techniques for circuits with level-sensitive memory elements. It contains a linear programming formulation applicable to the timing analysis of large scale circuits and includes a delay insertion methodology that improves the efficiency of clock skew scheduling. Coverage also provides a framework for and results from implementing timing optimization algorithms in a parallel computing environment.

This book presents MOSFET-based current mode logic (CML) topologies, which increase the speed, and lower the transistor count, supply voltage and power consumption. The improved topologies modify the conventional PDN, load, and the current source sections of the basic CML gates. Electronic system implementation involves embedding digital and analog circuits on a single die shifting towards mixed-mode circuit design. The high-resolution, low-power and low-voltage analog circuits are combined with high-frequency complex digital circuits, and the conventional static CMOS logic generates large current spikes during the switching (also referred to as digital switching noise), which degrade the resolution of the sensitive analog circuits via supply line and substrate coupling. This problem is exacerbated further with scaling down of CMOS technology due to higher integration levels and operating frequencies. In the literature, several methods are described to reduce the propagation of the digital switching noise. However, in high-resolution applications, these methods are not sufficient. The conventional CMOS static logic is no longer an effective solution, and therefore an alternative with reduced current spikes or that draws a constant supply current must be selected. The current mode logic (CML) topology, with its unique property of requiring constant supply current, is a promising alternative to the conventional CMOS static logic.

In response to tremendous growth and new technologies in the semiconductor industry, this volume is organized into five, information-rich sections. Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them. Featuring contributions from leading experts, the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies. Developing advanced concepts, this sharply focused book— Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits, whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state-of-the-art advances in fabrication and CMOS technologies Describes performance evaluation measures, which are the bottom line from the user's point of view Discusses design techniques used to create modern computer systems, including high-speed computer arithmetic and high-frequency design, timing and clocking, and PLL and DLL design

The Electrical Engineer's Handbook is an invaluable reference source for all practicing electrical engineers and students. Encompassing 79 chapters, this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students. This text will most likely be the engineer's first choice in looking for a solution; extensive, complete references to other sources are provided throughout. No other book has the breadth and depth of coverage available here. This is a must-have for all practitioners and students! The Electrical Engineer's Handbook provides the most up-to-date information in: Circuits and Networks, Electric Power Systems, Electronics, Computer-Aided Design and Optimization, VLSI Systems, Signal Processing, Digital Systems and Computer Engineering, Digital Communication and Communication Networks, Electromagnetics and Control and Systems. About the Editor-in-Chief... Wai-Kai Chen is Professor and Head Emeritus of the Department of Electrical Engineering and Computer Science at the University of Illinois at Chicago. He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems. He was Editor-in-Chief of the IEEE Transactions on Circuits and Systems, Series I and II, President of the IEEE Circuits and Systems Society and is the Founding Editor and Editor-in-Chief of the Journal of Circuits, Systems and Computers. He is the recipient of the Golden Jubilee Medal, the Education Award, and the Meritorious Service Award from the IEEE Circuits and Systems Society, and the Third Millennium Medal from the IEEE. Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science. * 77 chapters encompass the entire field of electrical engineering. * THOUSANDS of valuable figures, tables, formulas, and definitions. * Extensive bibliographic references.

System-on-a-chip (SoC) designs result in a wide range of high-complexity, high-value semiconductor products. As the technology scales towards smaller feature sizes and chips grow larger, a speed limitation arises due to an increased RC delay associated with interconnection wires. Innovative circuit techniques are required to achieve the speed needed for high-performance signal processing. Current sensing is considered as a promising circuit class since it is inherently faster than conventional voltage sense amplifiers. However, especially in SRAM, current sensing has rarely been used so far. Practical implementations are challenging because they require sophisticated analog circuit techniques in a digital environment. The objective of this book is to provide a systematic and comprehensive insight into current sensing techniques. Both theoretical and practical aspects are covered. Design guidelines are derived by systematic analysis of different circuit principles. Innovative concepts like compensation of the bit line multiplexer and auto-power-down will be explained based on theory and experimental results. The material will be interesting for design engineers in industry as well as researchers who want to learn about and apply current sensing techniques. The focus is on embedded SRAM but the material presented can be adapted to single-chip SRAM and to any other current-providing memory type as well. This includes emerging memory technologies like magnetic RAM (MRAM) and Ovonic Unified Memory (OUM). Moreover, it is also applicable to array like structures such as CMOS camera chips and to circuits for signal transmission along highly capacitive busses.

For the new millennium, Wai-Kai Chen introduced a monumental reference for the design, analysis, and prediction of VLSI circuits: The VLSI Handbook. Still a valuable tool for dealing with the most dynamic field in engineering, this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts, models, and equations. Written by a stellar international panel of expert contributors, this handbook is a reliable, comprehensive resource for real answers to practical problems. It emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus. WHAT'S IN THE SECOND EDITION? Sections on... Low-power electronics and design VLSI signal processing Chapters on... CMOS fabrication Content-addressable memory Compound semiconductor RF circuits High-speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages, expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions, The VLSI Handbook, Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice.

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed. In two editions spanning more than a decade, The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering. Our knowledge continues to grow, and so does the Handbook. For the third edition, it has grown into a set of six books carefully focused on specialized areas or fields of study. Each one represents a concise yet definitive collection of key concepts, models, and equations in its respective domain, thoughtfully gathered for convenient access. Combined, they constitute the most comprehensive, authoritative resource available. Circuits, Signals, and Speech and Image Processing presents all of the basic information related to electric circuits and components, analysis of circuits, the use of the Laplace transform, as well as signal, speech, and image processing using filters and algorithms. It also examines emerging areas such as text to speech synthesis, real-time processing, and embedded signal processing. Electronics, Power Electronics, Optoelectronics, Microwaves, Electromagnetics, and Radar delves into the fields of electronics, integrated circuits, power electronics, optoelectronics, electromagnetics, light waves, and radar, supplying all of the basic information required for a deep understanding of each area. It also devotes a section to electrical effects and devices and explores the emerging fields of microlithography and power electronics. Sensors, Nanoscience, Biomedical Engineering, and Instruments provides thorough coverage of sensors, materials and nanoscience, instruments and measurements, and biomedical systems and devices, including all of the basic information required to thoroughly understand each area. It explores the emerging fields of sensors, nanotechnologies, and biological effects. Broadcasting and Optical Communication Technology explores communications, information theory, and devices, covering all of the basic information needed for a thorough understanding of these areas. It also examines the emerging areas of adaptive estimation and optical communication. Computers, Software Engineering, and Digital Devices examines digital and logical devices, displays, testing, software, and computers, presenting the fundamental concepts needed to ensure a thorough understanding of each field. It treats the emerging fields of programmable logic, hardware description languages, and parallel computing in detail. Systems, Controls, Embedded Systems, Energy, and Machines explores in detail the fields of energy devices, machines, and systems as well as control systems. It provides all of the fundamental concepts needed for thorough, in-depth understanding of each area and devotes special attention to the emerging area of embedded systems. Encompassing the work of the world's foremost experts in their respective specialties, The Electrical Engineering Handbook, Third Edition remains the most convenient, reliable source of information available. This edition features the latest developments, the broadest scope of coverage, and new material on nanotechnologies, fuel cells, embedded systems, and biometrics. The engineering community has relied on the Handbook for more than twelve years, and it will continue to be a platform to launch the next wave of advancements. The Handbook's latest incarnation features a protective slipcase, which helps you stay organized without overwhelming your bookshelf. It is an attractive addition to any collection, and will help keep each volume of the Handbook as fresh as your latest research.

Very Large Scale Integration (VLSI) Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip. Emerging research in this area has the potential to uncover further applications for VLSI technologies in addition to system advancements. Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization. Through a research-based discussion of the technicalities involved in the VLSI hardware development process cycle, this book is a useful resource for researchers, engineers, and graduate-level students in computer science and engineering.

This book presents a collection of peer-reviewed articles from the 7th International Conference on Microelectronics, Circuits, and Systems Micro 2020. The volume covers the latest development and emerging research topics of material sciences, devices, microelectronics, circuits, nanotechnology, system design and testing, simulation, sensors, photovoltaics, optoelectronics, and its different applications. This book also deals with several tools and techniques to match the theme of the conference. It will be a valuable resource for researchers, professionals, Ph.D. scholars, undergraduate and postgraduate students working in Electronics, Microelectronics, Electrical, and Computer Engineering. The book provides a detailed analysis of issues related to sub-threshold interconnect performance from the perspective of analytical approach and design techniques. Particular emphasis is laid on the performance analysis of coupling noise and variability issues in sub-threshold domain to develop efficient compact models. The proposed analytical approach gives physical insight of the parameters affecting the transient behavior of coupled interconnects. Remedial design techniques are also suggested to mitigate the effect of coupling noise. The effects of wire width, spacing between the wires, wire length are thoroughly investigated. In addition, the effect of parameters like driver strength on peak coupling noise has also been analyzed. Process, voltage and temperature variations are prominent factors affecting sub-threshold design and have also been investigated. The process variability analysis has been carried out using parametric analysis, process corner analysis and Monte Carlo technique. The book also provides a qualitative summary of the work reported in the literature by various researchers in the design of digital sub-threshold circuits. This book should be of interest for researchers and graduate students with deeper insights into sub-threshold interconnect models in particular. In this sense, this book will best fit as a text book and/or a reference book for students who are initiated in the area of research and advanced courses in nanotechnology, interconnect design and modeling. The book will address the-state-of-the-art in integrated circuit design in the context of emerging systems. New exciting opportunities in body area networks, wireless communications, data networking, and optical imaging are discussed. Emerging materials that can take system performance beyond standard CMOS, like Silicon on Insulator (SOI), Silicon Germanium (SiGe), and Indium Phosphide (InP) are explored. Three-dimensional (3-D) CMOS integration and co-integration with sensor technology are described as well. The book is a must for anyone serious about circuit design for future technologies. The book is written by top notch international experts in industry and academia. The intended audience is practicing engineers with integrated circuit background. The book will be also used as a recommended reading and supplementary material in graduate course curriculum. Intended audience is professionals working in the integrated circuit design field. Their job titles might be : design engineer, product manager, marketing manager, design team leader, etc. The book will be also used by graduate students. Many of the chapter authors are University Professors.

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Using the book and the software provided with it, the reader can build his/her own tester arrangement to investigate key aspects of analog-, digital- and mixed system circuits Plan of attack based on traditional testing, circuit design and circuit manufacture allows the reader to appreciate a testing regime from the point of view of all the participating interests Worked examples based on theoretical bookwork, practical experimentation and simulation exercises teach the reader how to test circuits thoroughly and effectively

Spread in 133 articles divided in 20 sections the present treatises broadly discusses: Part 1: Image Processing Part 2: Radar and Satellite Image Processing Part 3: Image Filtering Part 4: Content Based Image Retrieval Part 5: Color Image Processing and Video Processing Part 6: Medical Image Processing Part 7: Biometric Part 8: Network Part 9: Mobile Computing Part 10: Pattern Recognition Part 11: Pattern Classification Part 12: Genetic Algorithm Part 13: Data Warehousing and Mining Part 14: Embedded System Part 15: Wavelet Part 16: Signal Processing Part 17: Neural Network Part 18: Nanotechnology and Quantum Computing Part 19: Image Analysis Part 20: Human Computer Interaction

Explains the circuit design of silicon optoelectronic integrated circuits (OEICs), which are central to advances in wireless and wired telecommunications. The essential features of optical absorption are summarized, as is the device physics of photodetectors and

their integration in modern bipolar, CMOS, and BiCMOS technologies. This information provides the basis for understanding the underlying mechanisms of the OEICs described in the main part of the book. In order to cover the topic comprehensively, Silicon Optoelectronic Integrated Circuits presents detailed descriptions of many OEICs for a wide variety of applications from various optical sensors, smart sensors, 3D-cameras, and optical storage systems (DVD) to fiber receivers in deep-sub- μ m CMOS. Numerous detailed illustrations help to elucidate the material.

The appropriate interconnect model has changed several times over the past two decades due to the application of aggressive technology scaling. New, more accurate interconnect models are required to manage the changing physical characteristics of integrated circuits. Currently, RC models are used to analyze high resistance nets while capacitive models are used for less resistive interconnect. However, on-chip inductance is becoming more important with integrated circuits operating at higher frequencies, since the inductive impedance is proportional to the frequency. The operating frequencies of integrated circuits have increased dramatically over the past decade and are expected to maintain the same rate of increase over the next decade, approaching 10 GHz by the year 2012. Also, wide wires are frequently encountered in important global nets, such as clock distribution networks and in upper metal layers, and performance requirements are pushing the introduction of new materials for low resistance interconnect, such as copper interconnect already used in many commercial CMOS technologies. On-Chip Inductance in High Speed Integrated Circuits deals with the design and analysis of integrated circuits with a specific focus on on-chip inductance effects. It has been described throughout this book that inductance can have a tangible effect on current high speed integrated circuits. For example, neglecting inductance and using an RC interconnect model in a production 0.25 μ m CMOS technology can cause large errors (over 35%) in estimates of the propagation delay of on-chip interconnect. It has also been shown that including inductance in the repeater insertion design process as compared to using an RC model improves the overall repeater solution in terms of area, power, and delay with average savings of 40.8%, 15.6%, and 6.7%, respectively. On-Chip Inductance in High Speed Integrated Circuits is full of design and analysis techniques for RLC interconnect. These techniques are compared to techniques traditionally used for RC interconnect design to emphasize the effect of inductance. emOn-Chip Inductance in High Speed Integrated Circuits will be of interest to researchers in the area of high frequency interconnect, noise, and high performance integrated circuit design.

Integrated Silicon Optoelectronics synthesizes topics from optoelectronics and microelectronics. The book concentrates on silicon as the major base of modern semiconductor devices and circuits. Starting from the basics of optical emission and absorption, as well as from the device physics of photodetectors, the aspects of the integration of photodetectors in modern bipolar, CMOS, and BiCMOS technologies are discussed. Detailed descriptions of fabrication technologies and applications of optoelectronic integrated circuits are included. The book, furthermore, contains a review of the newest state of research on eagerly anticipated silicon light emitters. In order to cover the topics comprehensively, also included are integrated waveguides, gratings, and optoelectronic power devices. Numerous elaborate illustrations facilitate and enhance comprehension. This extended edition will be of value to engineers, physicists, and scientists in industry and at universities. The book is also recommended to graduate students specializing on microelectronics or optoelectronics.

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